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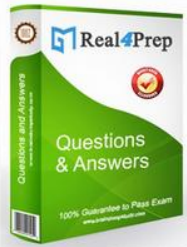
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Exam : **1z1-489**

Title : SPARC M6-32 and SPARC M5-32 Servers Installation Essentials

Vendor : Oracle

Version : DEMO

NO.1 You need to power down HOST 1 to add additional memory to a CMU. Which command do you execute?

- A. power -off /HOST1
- B. set keyswitch off /HOST1
- C. power /HOST1 off
- D. stop /HOST1

Answer: C

Explanation:

Reference:

http://docs.oracle.com/cd/E24355_01/html/E41214/z4000f4d1453042.html

NO.2 You are at a customer site to install some new PCIe cards into their SPARC M5-32 platform. You want to ensure there are no existing issues on the platform before you begin your work. Which command would you run to see details of the existing problems on the platform?

- A. show /System/Faults
- B. show /Servers/open_ problems
- C. Show /System/Open_prOblems
- D. show problems

Answer: C

Explanation:

Reference:

http://docs.oracle.com/cd/E24355_01/html/E41214/z40001331392991.html

NO.3 When booting up a pre-installed domain, you are asked for some configuration information. Where should you look for that information?

- A. System Administration Guide
- B. Installation and Configuration Plan
- C. Installation Guide
- D. Server Product Notes

Answer: C

Explanation:

Reference:

<https://www.debian.org/releases/stable/armel/install.txt.en>

NO.4 You have completed an OS installation in a PDom and, after subsequent reboots, you observe an error message regarding the pseudo- cdrom. Which command do you use to clear it?

- A. clear faults
- B. reset /HOSTx
- C. boot -r
- D. power off

Answer: C

Explanation:

Reference:

http://docs.oracle.com/cd/E19095-01/sfv890.svr/817-3956-12/system_overview.html

NO.5 A customer calls in and reports a problem with CMU#10. They said they have an issue with HOST1. What Domain Configurable Unit (DCU) is this board located in?

- A. DCU_0
- B. DCU_1
- C. DCU_2
- D. DCU_3
- E. DCU_4

Answer: B

Explanation:

Reference:

<https://docs.oracle.com/cd/E19874-01/820-4443-11/chapter1.html>

NO.6 A PCIe data path originates from a root complex (from the CPU) on a CMU that connects to a PCIE_SWITCH (in an IOB). Which two components is the signal fanned out to?

- A. PCIe slots
- B. EMS slots (that is, Base I/O board in IOU)
- C. CPU Memory Unit
- D. Scalability Switch Assembly

Answer: B,C

NO.7 Which three statements are true about the SPARC M5-32 server power grid connection?

- A. The server is designed to be powered by two utility power grids.
- B. You must connect three power cords (ACO, AC1, and AC2) to one power grid, and connect the remaining three power cords to a second power grid (AC3, AC4, and AC5).
- C. Only 3x power cords need to be connected when operating the server at half load.
- D. All six power cords must be connected, even if you are using only a single power grid.
- E. With dual-power feed set up, the first set of three power cords connected to the server are used to supply power, and the second set of three power cords are in standby mode.

Answer: A,B,E

Explanation:

Reference:

https://docs.oracle.com/cd/E19658-01/820-6175-12/install_pwr_on.html

NO.8 A customer reports that they have noticed an alert light on the CMU# 10. Which CPUs may be affected by issues within this CPU memory unit?

- A. CPU#10, CPU# 11, CPU#12, CPU#13
- B. CPU#22, CPU#23
- C. CPU#20, CPU#21,
- D. CPU#20, CPU=21

Answer: B

Explanation:

Reference:

https://docs.oracle.com/cd/E24355_01/html/E41214/bbgebhfe.html